UART setting:9600bps, 8bit, no parity, 1stop bit

0. initialize(buffer clear) command

...

1. Firmware version check

.hello<cr>

reply
:TerraBerryDAC II V02Lxx(YYYYMMDD)

xx is level
YYYYMMDD is Year Month Day

2. Serial port command echo control

.echo
.noecho

3. Serial port speed change

Change port speed 9600bps to 115200bps
Also need speed change RaspberryPi serial port after this command.
Keep 115200bps till reset or power off.

115200bps
.115200

4. DAC response terminate chractor

Change DAC messaege terminate chractor

CR/LF
.crlf (default)

CR
.cr

LF
.lf

5. DAC reset

Rset DAC MCU and seto intial value to SRC/DAC
Notice:Attenetor set 255 after reset.

.reset

6. DAC message control

DAC messate ON
.msgon

DAC message OFF (default)
.msgoff

7. DAC sound mode setting

.sound=[1|2|3]

1=Sound Setting 1 (default)
2=Sound Setting 2
2=Sound Setting 3

*See Table 27. SC1-0 bits Control of AK4490 data sheet

8. DAC filter setting

.filter=[0|1|2|3]

0=Sharp Roll-off Filter
1=Slow Roll-off Filter
2=Short delay Sharp Roll-off Filter (default)
3=Short delay Slow Roll-off Filter

*See Table 14. Digital Filter Setting of AK4490 data sheet

9. Attenuator setting

Increment ATT
.att+

Decrement ATT
.att-

ATT set 0xFF ト
.attf

ATT set 0
.att0

ATT set val

ex) set 100 to ATT
.att=100

10. DoP setting

SRC DoP decoder off
.dopoff

SRC DoP decoder on
.dopon

DoP decoder auto (default)
.dopauto

11. DSD gain setting

DSD out block gain -6dB ON(default)
.ogainon

DSD out block gain -6dB OFF
.ogainoff

DSD in gain 6dB ON
.igainon

DSD in gain 6dB OFF (default)
.igainoff

12. SRC/DAC mode setting

SRC DAC auto (default)
.dacauto

SRC PCMin/PCMout, DAC PCM mode
.dacpcm

SRC DSD(DoP ON)in/DSDout, DAC DSD mode
.dacdsd

SRC PCM(DoP OFF)in/DSDout, DAC DSD mode

.dacpcmdsd

SRC DSD(DoP ON)in/PCMout, DAC PCM mode
.dacdsdpcm

13. DAC DSDD setting

When DAC is DSD mode, bypass ATT

Bypass ATT
.dsddon

Enable ATT (default)
.dsddoff

14. SRC dither setting

SRC dither ON (default)
.dither ON

SRC dither OFF
.dither OFF

15. DAC mute control

.mon
.moff

16. SRC bypass control

This command SRC bypass mode cotrol.

SRC enable (default)
.bypsoff

SRC bypass for PCM48KHz, MCLK=49.152MHz
.bypspcm48

SRC bypass for PCM44.1KHz, MCLK=45.1584MHz
.bypspcm44

SRC bypass for DSD, MCLK=45.1584MHz
.bypsdsd